職稱	工作地點	科系	工作內容
數位 IC 設計工程 師	■台北 ■新竹 ■台南	程	1. Develop and implement the timing controller of TFT-LCD panel or relative 2. Digital IC design and simulation 3. FPGA verification and debugging/IP development 4. IC test pattern gereration or process mass production problem 5. High Speed IP design 6. AI deep learning Algorithm design 7. 3D depth decoder application design
類比 IC 設計工程	■台北■新竹■台南	電子/電機工程 相關科系	1. SERDES CMOS Circuit Design (HDMI, DisplayPort, or USB3.0). 2. All Digital PLL Circuit Design.
類比 IP 設計工程 師	■台北 ■新竹 ■台南	電子/電機工程 相關科系	1. PLL design 2. High speed receiver design (~3Gbps)/High speed transmitter design (~3Gbps) 3. eDP receiver/V-by-One receiver 4. MIPI D-PHY/HDMI Receiver/HDMI Transmitter/MHL Receiver 5. I/O (General purpose or high speed) design 6. Collaborate with the systems and applications group in the following fields: -Definition and design of IP spec.

-IP test and characterization	
-Systems and applications testing of product	
-Develop production test spec and implementation methods	